For Immediate Release   *Press Release*

DVCon 2012 Boasts Record Number of Paper Submissions, Announces Keynote Speaker and other Noteworthy Events

Louisville, CO – December 1, 2011 – The 2012 Design and Verification Conference (DVCon), sponsored by Accellera, received 145 paper submissions, a conference record. Twenty nine percent of them will be presented at the conference. Also growing is the number of companies that have registered to exhibit, which is up this year from the same period last year. DVCon has also added two co-located events. DVCon 2012 will be held February 27-March 1 at the DoubleTree Hotel in San Jose, California.

The 2012 keynote address, "Systemic Collaboration: Principles for Success in IC Design," will be presented by Aart de Geus, Chairman and CEO of Synopsys, Inc. at 2:00pm on February 29th.

"We continue to raise the bar for DVCon," stated Dr. Ambar Sarkar, DVCon 2012 Program Chair. "Engineers have come to rely on DVCon to inform and educate them on practical solutions, new experiences and ideas that will help them navigate the rough waters faced in design and verification. The value of DVCon is evident by the steady growth of the show and the large amount of high-quality session content our committee has to select from. This year we will see several papers and panels focused on the adoption of emerging standards in languages and methodologies, which is not surprising given the recent activities in Accellera. In addition, I am excited to see such a rich offering of papers and posters in areas such as mixed-signal verification, formal verification, debugging, and low-power techniques."

DVCon will provide two Poster Sessions again for 2012. Initiated last year, the Poster Sessions are intended to give attendees more opportunity to gather information on different areas of interest that may not be included in the more lengthy paper sessions. Poster Sessions will be presented on Tuesday and Wednesday during the conference.

Another testament to the quality of DVCon is the desire for companies and organizations to capitalize on the conference by co-locating an industry-related event. 2012 will have two co-located events. The EDAC Emerging Companies Committee has organized a panel, "Hardware/Software Co-design from a Software Perspective" to be held at the DoubleTree Hotel February 27th at 6:00pm. More information on this panel can be found at http://edac.org/events12/ecc/dvConPanel.jsp. NASCUG will also be hosting a SystemC Day on February 27th at the hotel. Call for presentations is open for the event. More information can be found at www.nascug.org.

The advance program for DVCon 2012 will be available December 5th at www.dvcon.org.

DVCon is the premier conference for discussion of the functional design and verification of electronic systems. DVCon is sponsored by **Accellera**, an industry consortium dedicated to the development and standardization of design and verification languages. For more information about Accellera, please visit www.accellera.org. For more information about DVCon, please visit www.dvcon.org. Follow @dvcon on Twitter or to comment, please use #dvcon.

For more information, please contact:

Kathy Embler	Barbara Benjamin
MP Associates, Inc.	HighPointe Communications
303-530-4562	503-209-2323
kathy@mpassociates.com	barbara@hipcom.com