Calypto Announces New President and CEO Sanjiv Kaul

SAN JOSE, Calif., – February 4, 2013 – Calypto® Design Systems, Inc., the leader in Electronic System Level (ESL) hardware design and Register Transfer Level (RTL) power optimization, today announced that Sanjiv Kaul has joined the company as President and Chief Executive Officer (CEO). Sanjiv brings a vast amount of experience and expertise to Calypto having held executive management and strategic marketing roles over the last 30 years including work at Synopsys, Frontline, Cadence/Valid Logic and Daisy.

"Calypto is in a very exciting juncture today and is in the strongest position in the history of the company," said Sanjiv Kaul, President and CEO at Calypto. "It has just completed a very successful year where Calypto achieved strong growth, record deals and substantial cash in the bank. With our best bookings year ever it is clear that Calypto is the number one player in the ESL hardware design and RTL power optimization space. As SoCs get ever more complex, design teams across the world are adopting Calypto's solutions to improve their design productivity. The merger of the original Calypto with the Catapult team from Mentor was the right strategic decision. The need for Calypto's HLS and RTL Power optimization has never been stronger and will continue to accelerate."

Sanjiv is passionate about finding fundamental changes in the market, knowing the right technology to answer to those changes, and then championing the technology along a successful adoption curve. While at Synopsys, Sanjiv was Sr. VP and GM for a number of high-profile products including Physical Compiler that enabled Synopsys to become the leader in the RTL synthesis to GDSII flow. He was also the Marketing Director for the launches and early market success for PrimeTime and Formality. Sanjiv is applauded for an approach to marketing and general management that is very realistic and results-oriented, building a solid franchise based on real value and customer satisfaction. After leaving Synopsys, Sanjiv was involved with several startups as Founder, Chairman, CEO, Investor and Board member.

About Calypto's Products

Catapult High Level Synthesis, Calypto's SLEC (Sequential Logic Equivalence Checking) and PowerPro platforms are used by seven out of the top ten semiconductor companies and over 100 leading consumer electronics companies worldwide. Calypto's products enable electronic system level design by engineers to dramatically improve design quality and reduce power consumption of their system-on-chip (SOC) devices.

About Calypto

Calypto® Design Systems, Inc. is the leader in ESL hardware design and RTL power optimization. Calypto, whose customers include Fortune 500 companies worldwide, is a member of the ARM Connected Community, Cadence Connections program, the IEEE–SA, Synopsys SystemVerilog Catalyst Program, the Mentor Graphics OpenDoor program, Si2 and is an active participant in the Power Forward Initiative. Calypto has offices in Europe, India, Japan and North America. More information can be found at www.calypto.com.

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Acronyms

AMBA: Advanced Microcontroller Bus Architecture
AXI: Advanced eXtensible Interface
ESL: Electronic System Level
HLS: High Level Synthesis
RTL: Register Transfer Level
SoC: System on Chip
TLM: Transaction Level Modeling