



IEEE Standards Association and Accellera Systems Initiative Team to Deliver Revised SystemVerilog Standard through IEEE Get Program

Standard for unified hardware design, specification, and verification of electronic systems now available to chip designers and engineers worldwide

San Jose, Calif., USA, 25 February 2013 (at the 2013 Design and Verification Conference) -- Accellera Systems Initiative (Accellera) announce today they have once again partnered with the IEEE Standards Association (IEEE-SA), a globally recognized standards-setting body within the IEEE, to deliver a leading electronic design and verification standard to engineers and chip designers worldwide. The revised version of the IEEE 1800™ "Standard SystemVerilog—Unified Hardware Design, Specification, and Verification Language Reference Manual" is now available through the IEEE Get Program, which grants public access to view and download select IEEE standards at no charge.

IEEE 1800 is the third standard to be delivered in partnership with the IEEE-SA Get program, joining two others: Get 1685™ Standard: IP-XACT, Standard Structure for Packaging, Integrating, and Reusing IP within Tool Flows, and Get 1666™ Standard: SystemC® Language Reference Manual.

"We are pleased to once again partner with the IEEE-SA to make this important standard available to the worldwide design community," said Shishpal Rawat, chair of Accellera. "Working together, we serve the global industry to deliver standards that increase designer productivity and lower the cost of product development."

"Through the support of the IEEE-SA, industry, and government, the IEEE Get program has become a key in standards adoption and implementation worldwide," said Konstantinos Karachalios, managing director of the IEEE-SA. "We applaud the dedicated work of Accellera and look forward to further strengthening our partnership to make other standards available through the Get program. It is through this type of collaborative effort that we serve the industry best and truly advance technology."

Get 1800™ Standard: SystemVerilog Language Reference Manual

Beginning immediately, companies, universities, research institutions, and individuals worldwide can freely access the standard and develop applications for SystemVerilog tools and technologies. To download a PDF copy, visit <http://standards.ieee.org/about/get/index.html>

About Accellera Systems Initiative

Accellera Systems Initiative (Accellera) is an independent, not-for profit organization dedicated to creating design and verification standards required by systems, semiconductor, intellectual property (IP), and electronic design automation (EDA) companies. The organization accelerates the development of standards that increase designer productivity and lower the cost of product development. As part of its ongoing partnership with the [IEEE](#), standards developed by Accellera are contributed to the IEEE Standards Association for formal standardization and ongoing governance. For more information, visit www.accelera.org. For membership information, [click here](#).

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