NEWS RELEASE

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Breker Verification Systems Enhances TrekSoC GUI

Enables Thorough Verification with Multi-Processor, Multi-Threaded C Test Cases

SAN JOSE, CALIF. — February 18, 2013 — Breker Verification Systems (www.brekersystems.com), The System-on-Chip (SoC) Verification Company, today unveiled an enhanced graphical user interface (GUI) for TrekSoC™, software that automatically generates self-verifying and synchronized C test cases to run on an SoC's multiple heterogeneous embedded processors for faster and more thorough verification.

TrekSoC produces multiple streams of real-world user scenarios and schedules the steps so that they cross threads and processors. Since these intertwined test cases are hard to follow, the new GUI features show clearly how the streams are scheduled across threads and how each is making progress in simulation. The GUI is part of TrekBox, the module of TrekSoC responsible for coordinating C test cases with activity in the testbench.

"In today's verification reality, no one can write complicated tests by hand, especially when they have multiple streams running in parallel on multiple processors," says Adnan Hamid, Breker's chief executive officer (CEO). "Verification engineers have pronounced Breker's test cases far more effective at stressing the SoC design than anything they could do by hand. The new GUI features allow them to see easily what's happening in the complex test cases generated by TrekSoC."

For example, a dual-processor digital camera SoC may have some user scenarios that must be able to run in parallel. TrekSoC will generate a test case for the user scenario to read an existing JPEG-encoded image from a USB port on one thread of the first processor. The test case will then decode the image on a thread of the second processor and display it on a screen using a different thread of the original processor. A TrekBox display showing this scenario is available at: http://www.brekersystems.com/products/treksoc#testcase.

TrekSoC will generate other parallel user scenarios within the same test case as specified by the scenario model that describes the SoC's functionality. Another scenario might capture a camera image, display it on a second screen, encode it to JPEG, and store the result on an SD card.

If the digital camera functionality is embedded within a smart phone, TrekSoC will generate a test case with even more user scenarios scheduled in parallel across all threads on all processors. It could process an incoming cellular call at the same time that an email message is arriving on the wireless Ethernet interface, just as an alarm goes off. Only through such stressful test cases can a complex SoC be adequately verified before fabrication.

TrekSoC and the enhanced TrekBox GUI will be demonstrated during DVCon February 26-27 from 3:30 p.m. to 6:30 p.m. at the DoubleTree Hotel in San Jose, Calif. Information about DVCon can be found at: www.dvcon.org.

Availability and Pricing

TrekBox comes standard with TrekSoC and is available now. Pricing is available upon request.

For more information, visit www.brekersystems.com.

About Breker Verification Systems

Breker Verification Systems is an Electronic Design Automation (EDA) software company that provides innovative solutions to solve the challenge of complex system-on-chip (SoC) functional verification. Its TrekSoC™ software and unique SoC scenario-modeling™ approach are used in production at leading semiconductor companies in the U.S., Europe and India. More information about Breker can be found at www.brekersystems.com or at https://www.facebook.com/pages/Breker-Verification-Systems/141262045996129. Daily updates on company activities are available at http://twitter.com/BrekerSystems and additional information can be found at http://www.linkedin.com/company/1010418. Founded in 2003, it is privately held and funded. Breker Verification Systems corporate headquarters is located at 1879 Lundy Ave., Suite 126, San Jose, Calif. 95131 Telephone: (650) 336-8872. Email: info@brekersystems.com. Website: www.brekersystems.com.

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